Advanced Process Control and Critical Dimension Control in Photolithography Process

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Article History

Abstract

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High volume, practical, assembling of cutting edge' photolithography processes requires top to bottom comprehension of Process and Process-Tool connection to accomplish Advanced Process Control (APC). The process likewise requires a tight control to keep up an ideal entryway CD. The direct loom that is right now being used in most semiconductor offices depends on administrator experience and doesn't give good control on the CD variety. Usage of a programmed input control framework in the business has been troublesome in light of the fact that the CD can't be estimated progressively finished. In this magazine, a neural complex is utilized to anticipate CD dependent on the estimations of thickness, reflectivity, refractive record, and portion. The neural network is prepared utilizing recorded information that are gathered at an assembling office. A neural scheme-based converse model of the progression is created. The opposite model is knock down with the progression model to frame a feed-forward controller. **Keywords:** Photolithography, Advanced Process Control, critical dimension, neural

I. Introduction

network

APC is utilized to control a process influenced by precise varieties. The segments of this control strategy are most normally separated into two principle control instruments, Run-to-Run (R2R), and Fault Detection Classification (FDC). Since both of these instruments address precise variety, device process models can be applied to mechanize process control. "R2R control of a process on an apparatus utilizes information from active and approaching wafers in mix with a model of the process in that instrument, to alter process boundaries" FDC then again, is the an expectation of inescapable device disappointment where the utilization of a suitable model offers grouping to a disappointment's underlying driver. Utilization of these control approachs isn't to state that conventional SPC strategies no longer assume a job in the present assembling, yet despite what might be expected, due to the way that SPC is planned to deal with irregular variety, it very well may be all the more properly applied with the precise variations represented by APC. APC has been applied to changing degrees in the semiconductor business, anyway the most prosperous assembling offices will be those that have the most elevated level of comprehension of how to incorporate APC into their creation stream.



Figure 1: General Advanced Process Control System Flow

The objective is to reach as continuous as workable for both R2R just as FDC. With this forceful objective, the device process boundaries can be balanced per wafer on account of R2R, and on account of FDC a possibly destructive blaming process can be ended and amended before processing any extra wafers. The procedure with increase in multifaceted nature of composed circuits joined with declining size of the unique circuit parts are setting extreme solicitations on production procedure. In the semiconductor business, the width of IC is implied as the basic measurement of the circuitry. convenient intrigue in the business to diminish planar circuits. It is basic to keep granular micro-lithographic procedure with the objective that the CD assortment is kept to a base. micro-lithographic procedure is a bit of the complete construction procedure of an organized circuit.

Neural enrolling approach gets cerebrum's responses for explicit issues and apply them to PC structures. The hypothesis limit tending to neural systems is a main issue in experienced planning. The diverse data limits that sway CD have a non-linear association with thems similarly likewise with the yield. The structure of the neural system relies truly upon authentic procedure data, with the enlightening record with no issue. Thusly, the neural system not simply gives the specific model of the earth where it is embedded, yet what's more plays out the information preparing limit of interest. Here, despite getting a familiar model of the micro-lithographic procedure, a neural system is used to show an opposite association. By falling the self-assured model with the opposite model a feed-forward regulator, as showed up in Fig 2, is obtained. An information CD regulator is then arranged which contains the feed-forward regulator with the feathery regulator in the analysis circle.



Figure 2: Block diagram representation of the feed-forward controller

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